

承 認 書 SPECIFICATION FOR APPROVAL

客戶名稱				
CUSTOMER	:			
客戶料號 CUSTOMER'S P/N	:		^	
料號 PART NUMBER	: <u>WAN007030</u> .	JD251SN04		
規格 DESCRIPTION	: Chip Antenna 7	030 M-Ant 2.45G	+5G Type 04	
版本 VERSION	: <u>V1.1</u>			
日期 ISSUE DATE	: 2023/06/15	~	KD,	
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	.0	工 程 部 R&D CENTER		
	承 認 APPROVAL	確 認 CHECKED	製 作 DRAWN	
	Ray	Snow	Jerry	





萬誠科技股份有限公司

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OneWave Electronic Co., Ltd.

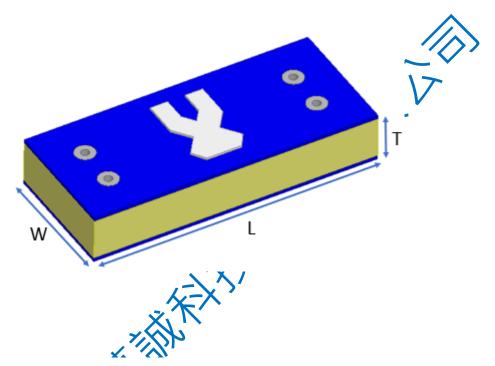
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7030 Chip antenna

For WLAN Dual-Band Applications



P/N: WAN007030JD251SN04

Ovensy

	Dimension (mm)		
L	7.00 ± 0.20		
W	3.00 ± 0.20		
T	1.15 ± 0.20		



Part Number Information

WAN 00 7030 J D25 1S N 04 F

Product Series	Antenna		
Dimension L x W	7.0X3.0mm (+-0.2mm)		
Material	High K material		
Working Frequency	2.4 ~ 2.5GHz + 5.15~5.85GHz		
Feeding mode	Monopole & Single Feeding		
Antenna type	Type = 04 \\ \\ \		
	Dimension L x W Material Working Frequency Feeding mode		

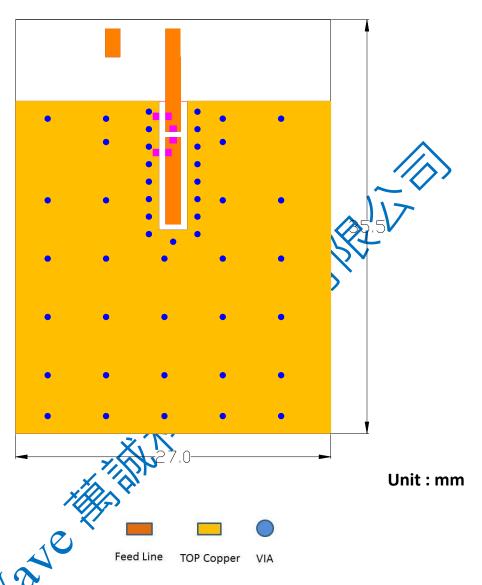
1. Electrical Specification

Specification			
Part Number	WAN007030JD251SN04		
Central Frequency	2450 / 5500	MHz	
Bandwidth	100 / 800 (Min.)	MHz	
Return Loss	-10 (Max)	dB	
Peak Gain	2.95 / 5.40	dBi	
Impedance	50	Ohm	
Operating Temperature	-40~+110	$^{\circ}$ C	
Maximum Power	4	W	
Resistance to Soldering Heats	10 (@ 260℃)	sec.	
Polarization	Linear		
Azimuth Beamwidth	Omni-directional		
Termination	Cu / Sn (Leadless)		

Remark: Bandwidth & Peak Gain was measured under evaluation board of next page



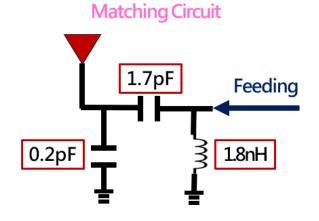
2. Recommended PCB Pattern Evaluation Board Dimension



Suggested Matching Circuit

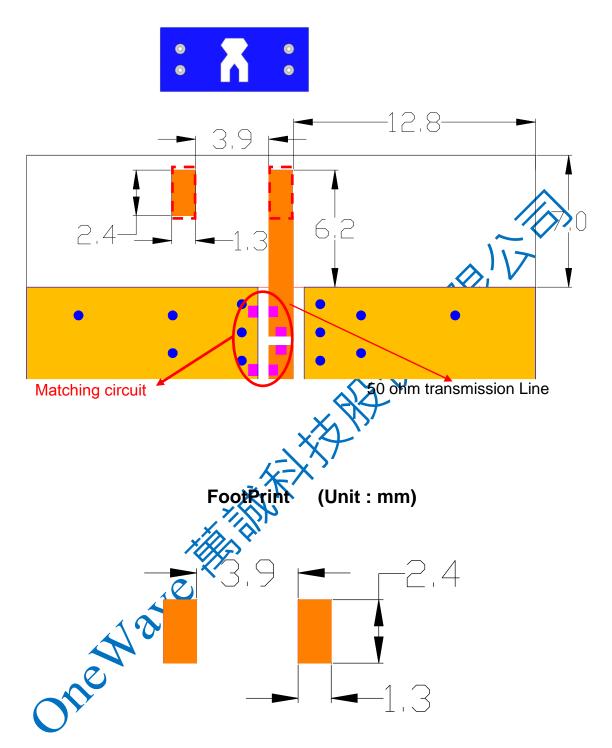
重要資訊

匹配允件建議使用精準度高的電感±0.1~0.3nH、電容±0.1pF





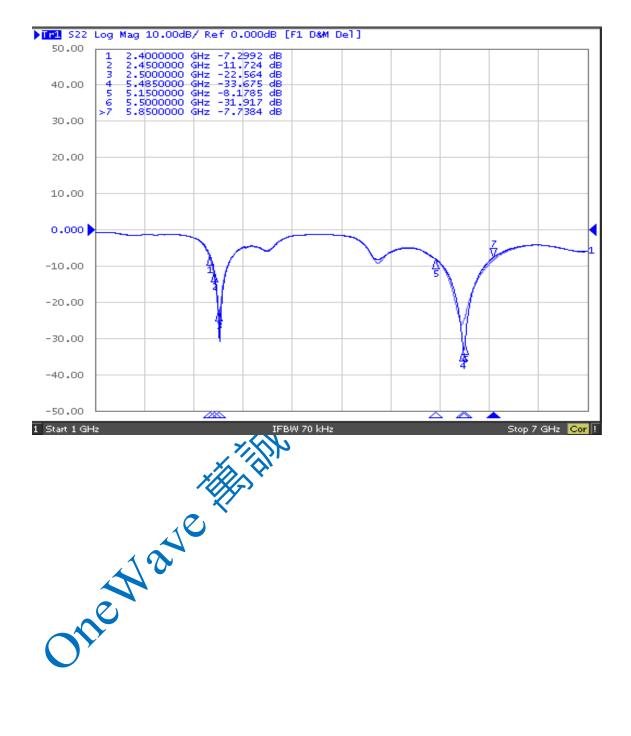
♦ Layout Dimensions in Clearance area(Size=27.0*7.0mm)



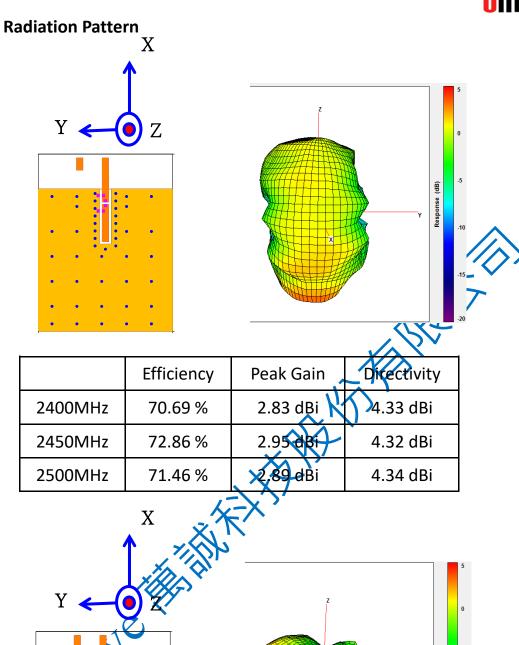


3. Measurement Results

Return Loss



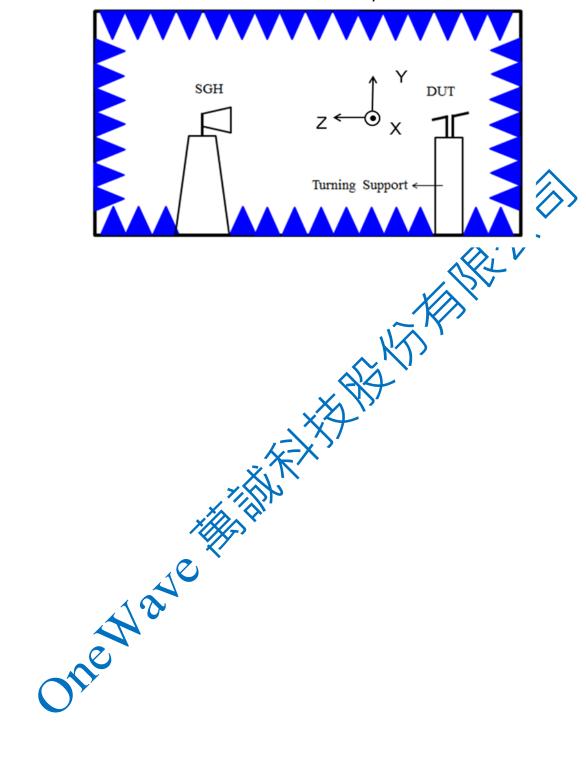




	Efficiency	Peak Gain	Directivity
5150MHz	75.83 %	5.28 dBi	6.48 dBi
5500MHz	78.80 %	5.40 dBi	6.43 dBi
5850MHz	76.44 %	5.34 dBi	6.50 dBi



Chamber Coordinate System





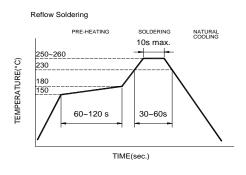
4. Reliability and Test Condictions

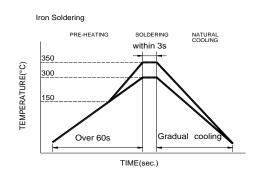
ITEM	REQUIREMENTS	TEST CONDITION	
Solderability	1. Wetting shall exceed 90% coverage 2. No visible mechanical damage TEMP (°C) 230°C 4±1 sec.	Pre-heating temperature:150°C/60sec. Solder temperature:230±5°C Duration:4±1sec. Solder:Sn-Ag3.0-Cu0.5 Flux for lead free: rosin	
	150°C 60sec		
Solder heat Resistance	1. No visible mechanical damage 2. Central Freq. change :within ± 6% TEMP (°C) 260°C 150°C 150°C	Pre-heating temperature:150°C/60sec. Solder temperature:260±5°C Duration:10±0.5sec. Solder:Sn-Ag3.0-Cu0.5 Flux for lead free: rosin	
Component Adhesion (Push test)	No visible mechanical damage	The device should be reflow soldered(280±5°C for 10sec.) to a tinned copper substrate A dynometer force gauge should be applied the side of the component. The device must with-ST-F 0.5 Kg without failure of the termination attached to component.	
Component	No visible mechanical damage	Insert 10cm wire into the remaining open	
Adhesion	_\X/	eye bend ,the ends of even wire lengths	
(Pull test)	XXXX	upward and wind together. Terminal shall not be remarkably damaged.	
Thermal shock	No visible mechanical damage	+110°C=>30±3min	
	2. Central Freq. change :within ±6%	-40°C=>30±3min	
	Phase Temperature(°C) Time(min)	Test cycle:10 cycles	
	1 +110±5°C 30±3	The chip shall be stabilized at normal	
	2 Room Within	condition for 2~3 hours before measuring.	
	Temperature 3sec	modeling.	
	3 -40±2°C 30±3		
	4 Room Within		
	Temperature 3sec		
Resistance to	1. No visible mechanical damage	Temperature: +110±5°C	
High	2. Central Freq. change :within ±6%	Duration: 1000±12hrs	
Temperature	No disconnection or short circuit.	The chip shall be stabilized at normal condition for 2~3 hours before measuring.	
Resistance to	No visible mechanical damage	Temperature:-40±5°C	
Low	Central Freq. change :within ±6%	Duration: 1000±12hrs	
Temperature	3. No disconnection or short circuit. 3. No disconnection or short circuit.	The chip shall be stabilized at normal condition for 2~3 hours before measuring.	
Humidity	No visible mechanical damage	Temperature: 40±2°C	
	2. Central Freq. change :within ±6%	Humidity: 90% to 95% RH	
	3. No disconnection or short circuit.	Duration: 1000±12hrs	
		The chip shall be stabilized at normal condition for 2~3 hours before measuring.	



5. Soldering and Mounting

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.





Recommended temperature profiles for reflow soldering in Figure 1.

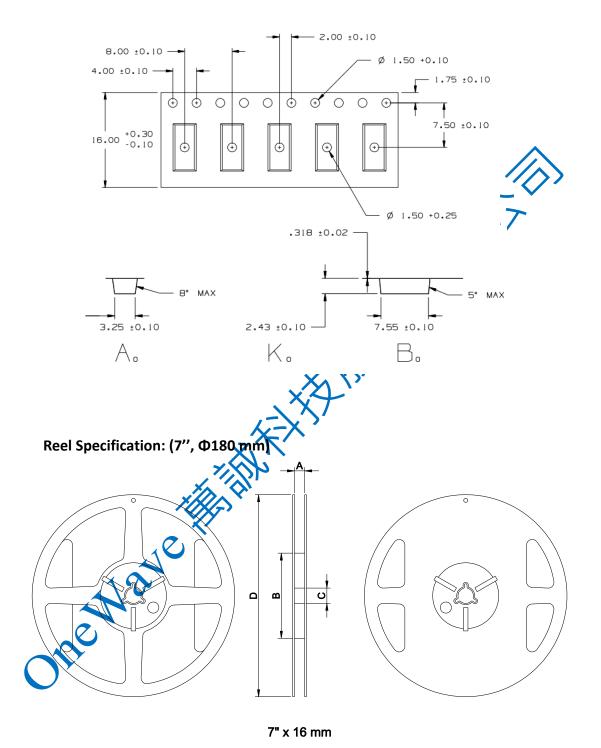
Products attachment with a soldering from s discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precaptions are recommended.

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 280°C tip temperature (max)
- Omm tip diameter (max)
- · Limit soldering time to 3 sec.



6.Packaging Information

♦ Tape Specification:



Tape Width(mm)	A(mm)	B(mm)	C(mm)	D(mm)	Chip/Reel(pcs)
16	16±1.0	60±2	13.5±0.5	178±2	2000



7. Storage and Transportation Information

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. Temperature and humidity conditions: -10~ 40°C and 30~70% RH.
- 2. Recommended products should be used within 6 months from the time of delivery.
- 3. The packaging material should be kept where no chlorine or sulfur exists in the air.

Transportation Conditions

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- Products should be handled with care to avoid damage of contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.